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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/759,239
Filing Date	January 20, 2004
First Named Inventor	Ji-Young KIM, et al.
Art Unit	2814
Examiner Name	Long PHAM
Attorney Docket Number	253/023

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U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

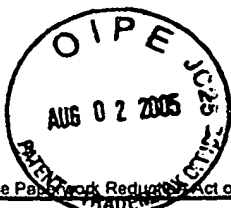
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ²
		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)				
LP	5	JP 11-150265 *	06-02-1999	AGAWA		
		* with English				
		Abstract				

Examiner Signature	12/16 Phqm	Date Considered	8/12/05
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PTO/SB/08B (04-03)

Approved for use through 04/30/2003. OMB 0651-0031

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of

2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
LP	6	CHOI, et al., "A Spacer Patterning Technology for Nanoscale CMOS",	
LP		IEEE Transactionson Electron Devices, Vol. 49, No. 3, pp. 436-441 (March 2002)	

Examiner Signature	Long Pham	Date Considered	8/12/05
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